

## ECE : LINEAR INTEGRATED CIRCUITS

**Programme:** B.Tech. ECE  
**Course Type:** Programme Elective

**Year:** 3<sup>rd</sup>  
**Credits:** 3

**Semester:** Odd  
**Hours:** 40

### Course Context and Overview (100 words):

This course is to teach the fundamentals of Integrated Circuits and basic blocks to build integrated circuits. Topics to be covered include IC fabrication steps, circuit configurations to build an IC, amplifiers behavior at low and high frequency, operational amplifier and its application, PLL and Bi-CMOS circuits. The course also includes introduction to Spice language including few design examples and simulations.

**Prerequisites Courses:** Semiconductor Devices & Circuits, Analog Electronics

### Course outcomes (COs):

<b>On completion of this course, the students will be able</b>
<b>CO1:</b> To understand the basic building blocks of linear integrated circuits.
<b>CO2:</b> To gain in-depth knowledge of operational amplifier and its application.
<b>CO3:</b> To gain knowledge in PLL and Bi-CMOS logic.
<b>CO4:</b> To understand the Spice programming software.

### Course Topics:

Topics	Lecture Hours
<b>UNIT - I</b>	<b>8</b>
<b>1. Integrated Circuit</b>	
1.1 Manufacturing Process of IC	2
1.2 Construction & working of BJT and FET devices	4
1.3 Integrated circuit design and complexity	2
<b>UNIT - II</b>	<b>12</b>
<b>2. Amplifiers</b>	
2.1 Single Stage CS amplifier, Types of load, Gain calculation, Low frequency and High frequency behavior, Gain boosting technique	5
2.2 Differential amplifiers, Differential amplifier with and without Active load, performance parameters, Low frequency and High frequency behavior	5
2.3 Two-stage CMOS op-amp, Frequency compensation	2
<b>UNIT-III</b>	<b>7</b>
<b>3. Analysis and Applications of OP-AMP</b>	
3.1 Op-amp parameters	1
3.2 Open loop and Closed loop op-amp configurations	1
3.3 Inverting & Non-inverting modes	1
3.4 Applications of op-amp in Active filters, Instrumentation amp, I-V and V-I converters, Log and Anti-log amplifiers etc.	4

<b>UNIT-IV</b>		
<b>4. Phase Locked Loop &amp; Bi-CMOS</b>	<b>9</b>	
<b>4.1 PLL: Basic PLL topology, Voltage controlled oscillator, Charge pump PLL, DLL, Applications</b>	<b>5</b>	
<b>4.2 Bi-CMOS: Basic understanding, Performance comparison of CMOS &amp; Bi-CMOS, Applications</b>	4	
<b>UNIT-V</b>		
<b>5. SPICE Programming</b>	<b>4</b>	
<b>5.1 Introduction to Spice, Structure &amp; statements</b>	<b>1</b>	
<b>5.2 Basic Analyses Types: DC, AC &amp; Transient</b>	<b>2</b>	
<b>5.3 Simulation examples</b>	1	

**Text Books:**

- [1] Linear Integrated Circuits, S Salivahaan and V S Kanchana Bhaaskaran, 2<sup>nd</sup> Ed., McGraw-Hill, 2015.  
 [2] Design of Analog CMOS Integrated Circuits, B. Razavi, McGraw-Hill, 2001.  
 [3] Design with Operational Amplifiers and Analog Integrated Circuits, Sergio Franco, 4th Ed., McGraw-Hill, 2014.

**Reference book:**

- [1] OP-AMP and Linear ICs, Ramakant A. Gayakwad, 4th Edition, Prentice Hall Pearson Education, 2001.  
 [2] Operational Amplifiers and Linear Integrated Circuits, Robert F. Coughlin and Frederick F. Driscoll, PHI, 2001.

**Additional Resources (Web resources etc.):**

<http://www.ee.iitm.ac.in/videlectures>

<https://www.sciencedirect.com/science/article/abs/pii/0026269289901237>

**Evaluation Method:**

Item	Weightage (%)
Assignments/Quiz	20
Mid Term	30
End Term	50

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